UCLM
Universidad de Castilla~La Mancha

Escuela Politécnica

30º Ciclo de Conferencias
Información Técnica

TerosHDL `v6.0.10`
Teros Technology | ⌄ 84,699 | ★★★★★ (27)

Powerful toolbox for ASIC/FPGA: state machine viewer, linter, documentation, snippets... and more!

Install ✓ Auto Update ⚙

DETAILS   FEATURES

version `v6.0.10`   installs `85k`   vs marketplace   rate limited by upstream service   vscode-TerosHDL Documentation `up`

TerosHDL
An open source Toolbox for FPGA/ASIC

Check the full documentation: https://terostechnology.github.io

Desarrollo ágil de FPGA con TerosHDL y herramientas open source

Presenta: Carlos Alberto Ruiz Naranjo
TerosHDL

**Jueves
21/11/2024
Politécnica
Campus Cuenca**

Online -Teams-
https://cutt.ly/HeUUhlru

12:30-14:00 h.

30 ANIVERSARIO
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